In re PATENT APPLICATION of TSUJI et al. Inventor(s):

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Group Art Unit:

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Filed: June 22, 2001

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Title: MANAGING APPARATUS AND MANAGING METHOD

OF A SEMICONDUCTOR MANUFACTURING APPARATUS

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October 15, 2001

SUBMISSION OF PRIORITY **DOCUMENT IN ACCORDANCE** WITH THE REQUIREMENTS OF RULE 55

Hon. Asst Commissioner of Patents Washington, D.C. 20231

Sir:

Please accept the enclosed certified copy(ies) of the respective foreign application(s) listed below for which benefit under 35 U.S.C. 119/365 has been previously claimed in the subject application and if not is hereby claimed.

Application No.

Country of Origin

Filed

2000-153152

Japan

May 24, 2001

Respectfully submitted,

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PATENT OFFICE JAPANESE GOVERNMENT

This is to certify that the annexed is a true copy of the following application as filed with this office.

Date of Application:

May 24, 2000

Application Number:

Japanese Patent Application

No. 2000-153152

Applicant(s):

TOKYO ELECTRON LIMITED

June 4, 2001

Commissioner,

Patent Office

Kouzo Oikawa (Seal)

Certificate No.2001-3052072



日本国特許庁 JAPAN PATENT OFFICE

別紙添付の書類に記載されている事項は下記の出願書類に記載されて いる事項と同一であることを証明する。

This is to certify that the annexed is a true copy of the following application as filed with this Office

出願年月日 Date of Application:

2000年 5月24日

出 願 番 号
Application Number:

特願2000-153152

出 **顏** 人 Applicant(s):

東京エレクトロン株式会社

2001年 6月 4日

特許庁長官 Commissioner, Japan Patent Office



